



5A, 1200V Silicon Carbide Schottky Diode

Features

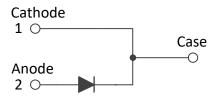
- High-Frequency Operation
- Zero Reverse Recovery Current
- Temperature-Independent Switching
- Extremely Fast Switching
- Plastic package has underwriters Laboratory
 Flammability Classification 94V-0
- Halogen-free according to IEC 61249-2-21

Applications

- Boost Diodes in PFC or DC/DC stages
- LED Lighting Power Supplies
- Power Factor Correction



TO-252(D-PAK)



Mechanical Data

- Case: Epoxy, Molded
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 sec
- Shipped 2500 units per reel

Maximum Ratings & Electrical Characteristics(TA=25°C unless otherwise noted)					
Parameter	Symbol	GS05D120SD	Unit		
Maximum repetitive peak reverse voltage	VRRM	1200	V		
Working peak reverse voltage	Vrwm	1200	V		
Maximum DC blocking voltage	VDC	1200	V		
	Tc=25°C		20	А	
Maximum average forward rectified current	Tc=135°C	lF(AV)	9.4		
	Tc=157°C		5		
Peak forward surge current, tp=10ms,Half Sir	IFSM	IFSM 55			
Dougs discination	Tc=25°C	Ptot	115	W	
Power dissipation	Tc=110°C	Ptot	50	VV	
Operating junction temperature range	TJ	TJ -55 to +175			
Storage temperature range	Тѕтс	-55 to +175	°C		



Electrical Specifications(Ta=25°C unless otherwise noted)						
Parameter	Symbol	Test Conditions	Тур	Max	Unit	
Forward drap voltage	VF	IF=5A, TJ=25°C	1.40	1.70	V	
Forward drop voltage		IF=5A, TJ=175°C	2.00	2.50		
Poverse leakage current @rated Vp	lr	V _R =1200V, T _J =25°C	2	100	μΑ	
Reverse leakage current @rated VR	IK	V _R =1200V, T _J =175°C	11	300		
Total capacitive charge		VR=800V, IF=5A, TJ=25°C	36	ı	nC	
Total capacitance	С	V _R =800V, T _J =25°C, f=1MHz	26	1	pF	

Thermal-Mechanical Specifications (TA=25°C unless otherwise noted)				
Parameter	Symbol	Тур	Max	Unit
Thermal Resistance, Junction to Case	Rejc	1.30	-	°C /W





Ratings and Characteristics Curves

(TA = 25°C unless otherwise noted)

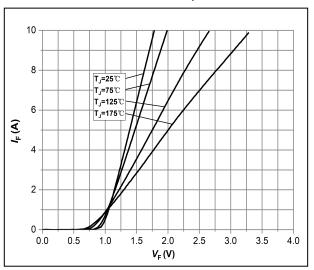


Fig.1 -Forward Characteristics

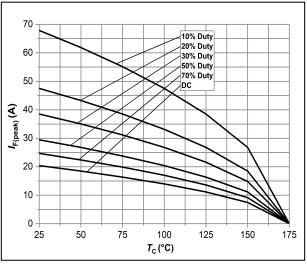


Fig.3 -Current Derating

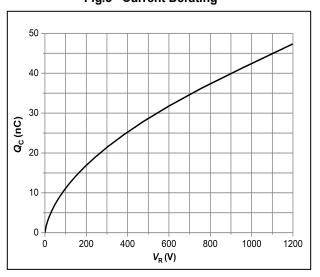


Fig.5 -Total Capacitance Charge vs. Reverse Voltage

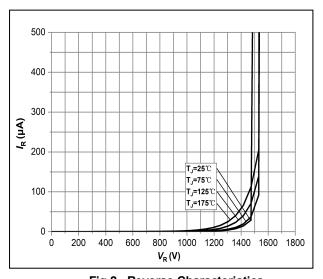


Fig.2 -Reverse Characteristics

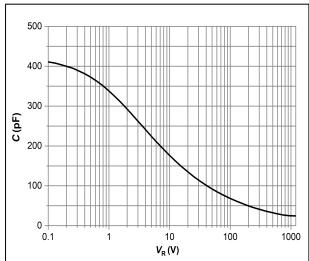


Fig.4 - Capacitance vs. Reverse Voltage

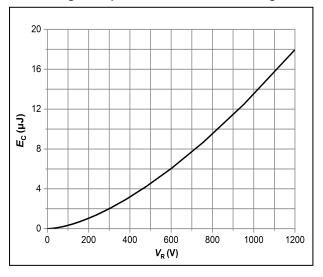
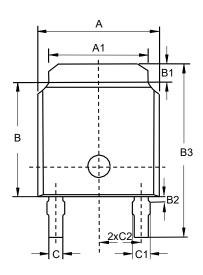


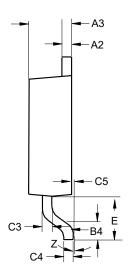
Fig.6 -Typical Capacitance Stored Energy

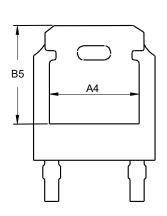


Package Outline Dimensions (Unit: millimeters)

TO-252 (D-PAK)



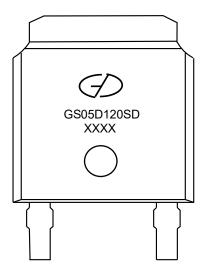




TO-252							
	Min.	Nom.	Max.		Min.	Nom.	Max.
Α	6.40	6.60	6.731	B5	5.21		
A1	5.21	5.34	5.46	С	0.64	0.76	0.88
A2	0.46	0.50	0.58	C1	0.77	0.84	1.14
A3	2.20	2.30	2.38	C2	2.886BSC		
A4	4.40			C3	0.46	0.50	0.60
В	6.00	6.10	6.223	C4	0.508BSC		
B1	0.89		1.27	C5	0		0.127
B2				Е	2.743REF		
В3	9.40	10.0	10.40	Z	0°		10°
B4	1.40	1.52	1.77				



Marking Outline



1. Logo Mark:

2. Part Name: GS05D120SD

3. Date Code: XXXX

Revision History

Document Version	Date of release	Description of changes		
Rev.A 2022.06.30		Preliminary Datasheet		





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