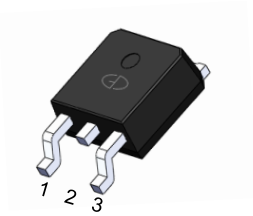
580mΩ,650V (D-S) Super Junction Power MOSFET

Features

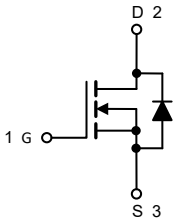
- 100% Avalanche Tested
- Extremely Low Losses with Low FOM Rdson*Qg
- Halogen Free, Pb-Free
- RoHS Compliant



TO-252 (D-PAK)

Applications

- Solar inverter
- Telecom/Sever
- AC/DC power supply



Absolute Maximum Ratings (T_J=25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain Source Voltage		V_{DS}	650	V
Gate Source Voltage		V_{GS}	±30	V
Drain Current, Continuous V_{GS}=10V	T_C=25°C	I_D	12	A
	T_C=100°C		8	
Drain Current, Pulsed *(Note 1)*		I_{DM}	48	A
Single Avalanche Energy *(Note 2)*		E_{AS}	120	mJ
Power Dissipation	T_C=25°C	P_D	179	W
	T_C=100°C		71	
Operating Junction/ Storage Temperature Range		T_J/ T_{STG}	-55 to +150	°C

Note 1: Single pulse; t_p ≤ 1us.
Note 2: V_{DD} = 50V, I_{AS} = 2A, L = 60mH, R_G = 25Ω, starting T_J = 25 °C.

Thermal Characteristics

Parameter	Symbol	Max	Unit
Thermal Resistance Junction to Case	R_{thJC}	0.7	°C/W
Thermal Resistance Junction to Ambient *(Note 3)*	R_{thJA}	62.5	°C/W

Note 3: Device mounted on 1 square inch FR4 PCB board, with 2oz single-sided copper, in a 25°C still air environment.

Electrical Characteristics (T_J =25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=250μA	650	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=650V, V_{GS}=0V	--	--	1	uA
Gate Threshold Voltage	$V_{GS(TH)}$	V_{DS}=V_{GS}, I_{DS}=250uA	2	3.2	4	V
Gate Leakage Current	I_{GSS}	V_{GS}=±30V, V_{DS}=0V	--	--	±100	nA
Drain-Source On-state Resistance *(Note 4)*	$R_{DS(on)}$	V_{GS}=10V, I_D=4A	--	480	580	mΩ
Total Gate Charge	Q_g	$V_{GS(off)}$=0V, $V_{GS(on)}$=10V, V_{DD}=325V, I_D=10A	--	15	--	nC
Gate-Source Charge	Q_{gs}		--	2	--	
Gate-Drain Charge	Q_{gd}		--	10	--	
Turn-on Delay Time	$t_{d(on)}$	V_{GS}=10V, V_{DD}=325V, R_L=0.75Ω, R_G=10Ω	--	45	--	ns
Turn-on Rise Time	t_r		--	28	--	
Turn-off Delay Time	$t_{d(off)}$		--	57	--	
Turn-off Fall Time	t_f		--	25	--	
Gate Resistance	R_g	V_{GS}=0V, f=1MHz, open drain	--	22	--	Ω
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=650V, f=1MHz	--	484	--	pF
Output Capacitance	C_{oss}		--	18	--	
Reverse Transfer Capacitance	C_{rss}		--	4	--	

Reverse Diode Characteristics (T_J =25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Forward Current, Continuous	I_{SD}	T_C=25°C	--	--	12	A
Diode Forward Voltage *(Note 4)*	V_{SD}	I_F=10A, V_{GS}=0V	--	--	1.2	V
Reverse Recovery Time	T_{rr}	V_R =325V, I_F =2A, di/dt = 180 A/μs	--	116	--	ns
Reverse Recovery Charge	Q_{rr}		--	827	--	nC

Note 4: Pulse test; pulse width ≤ 380μs, duty cycle ≤ 1%.

Typical Characteristics Curves (T_J = 25℃ unless otherwise noted)

Fig. 1 - Output Characteristics

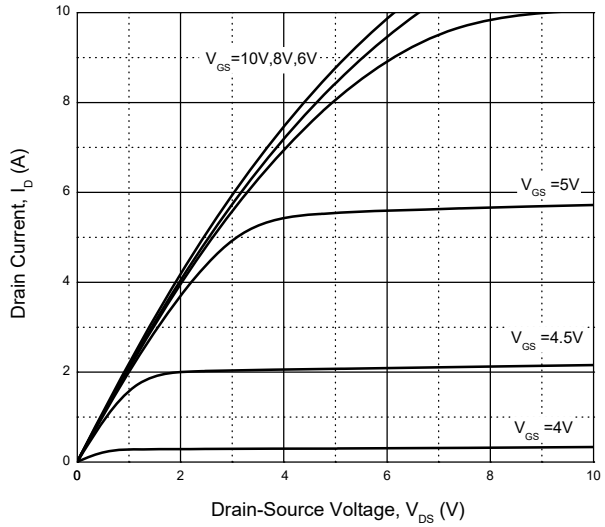


Fig. 2 - Transfer Characteristics

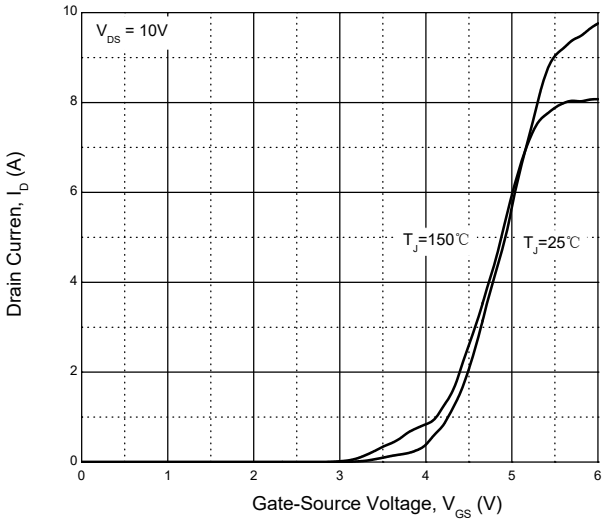


Fig. 3 - Drain-Source On-Resistance

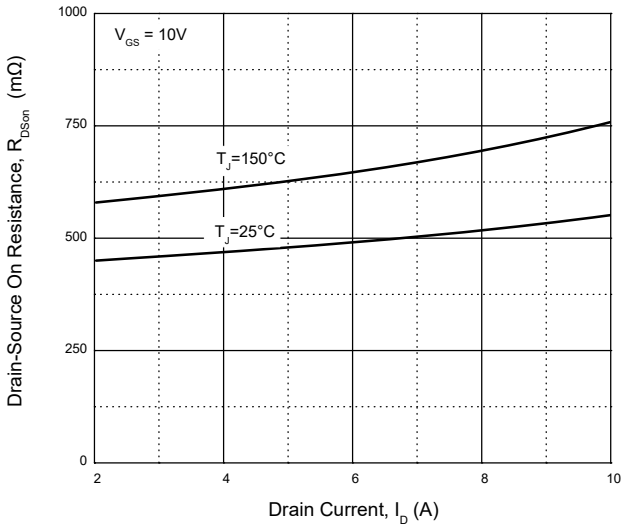


Fig. 4 - Normalized On-Resistance

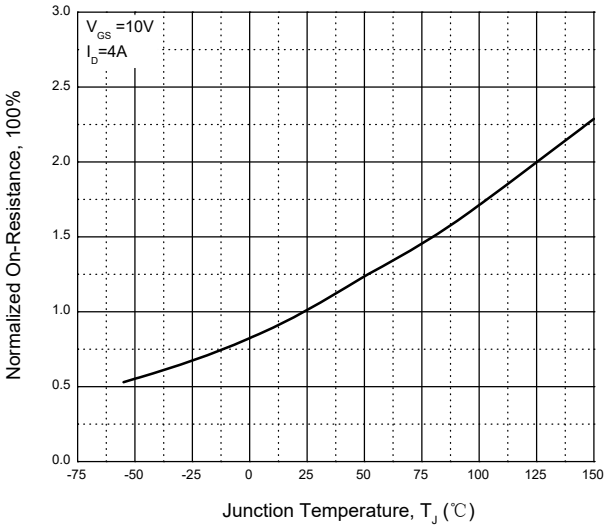


Fig. 5 - Drain-Source On-Resistance

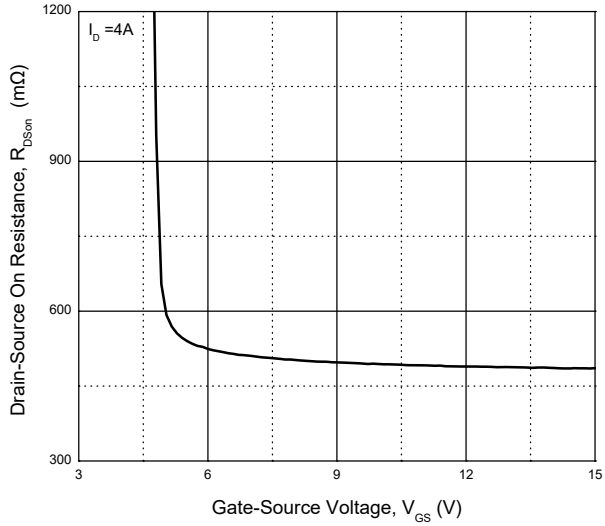
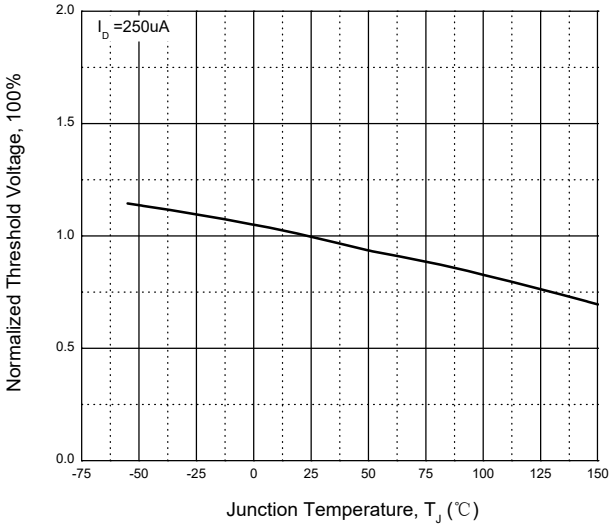


Fig. 6 - Normalized Threshold Voltage



Typical Characteristics Curves (T_J = 25℃ unless otherwise noted)

Fig. 7 - Capacitance

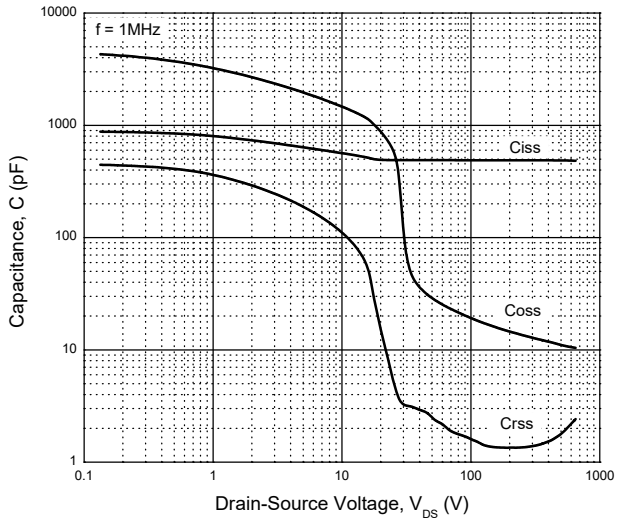


Fig. 8 - Gate Charge

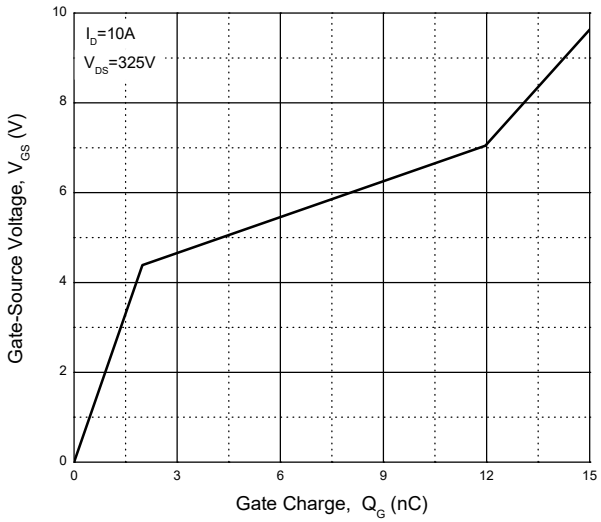


Fig. 9 - Forward Characteristic

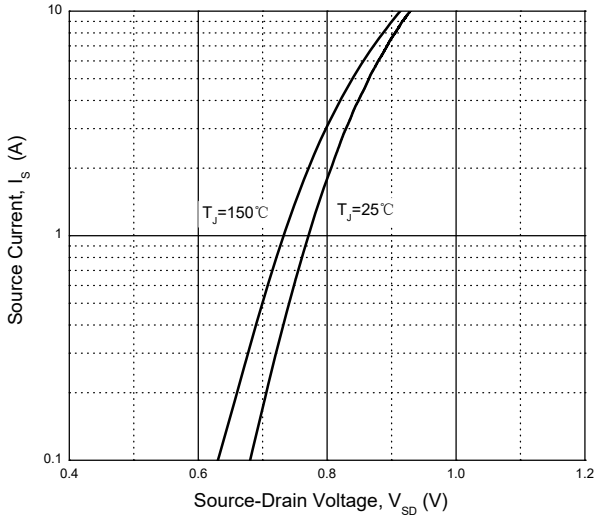


Fig. 10 - Safe Operating Area

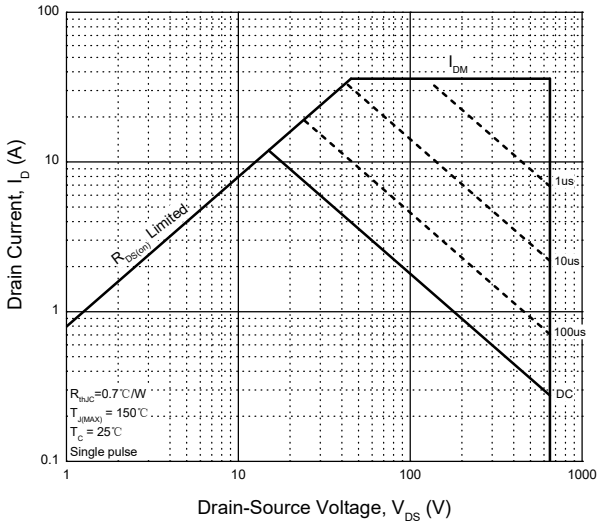
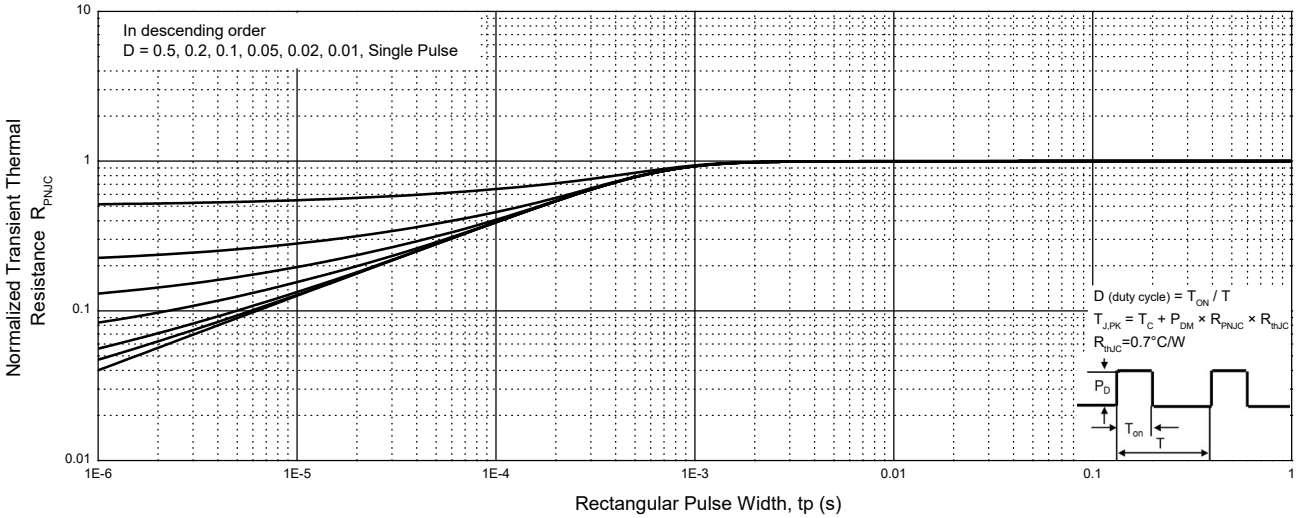


Fig.11 - Normalized Thermal Impedance, Junction-Case



Typical Characteristics Curves (TJ = 25°C unless otherwise noted)

Fig. 12 - Power Derating

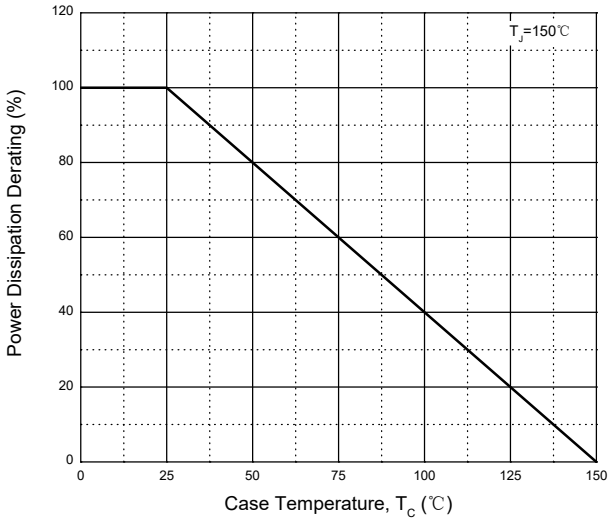
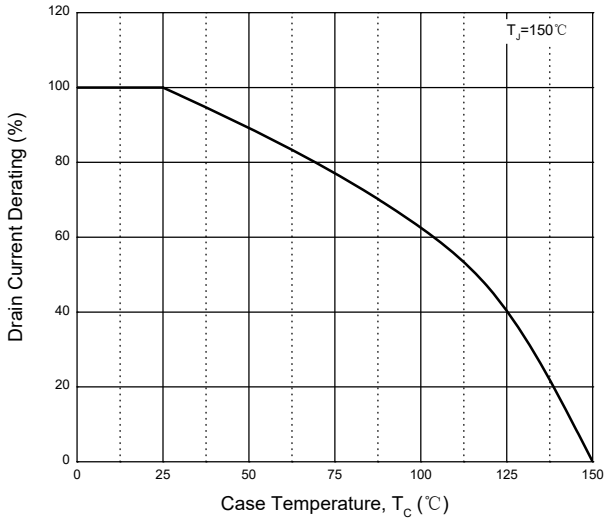
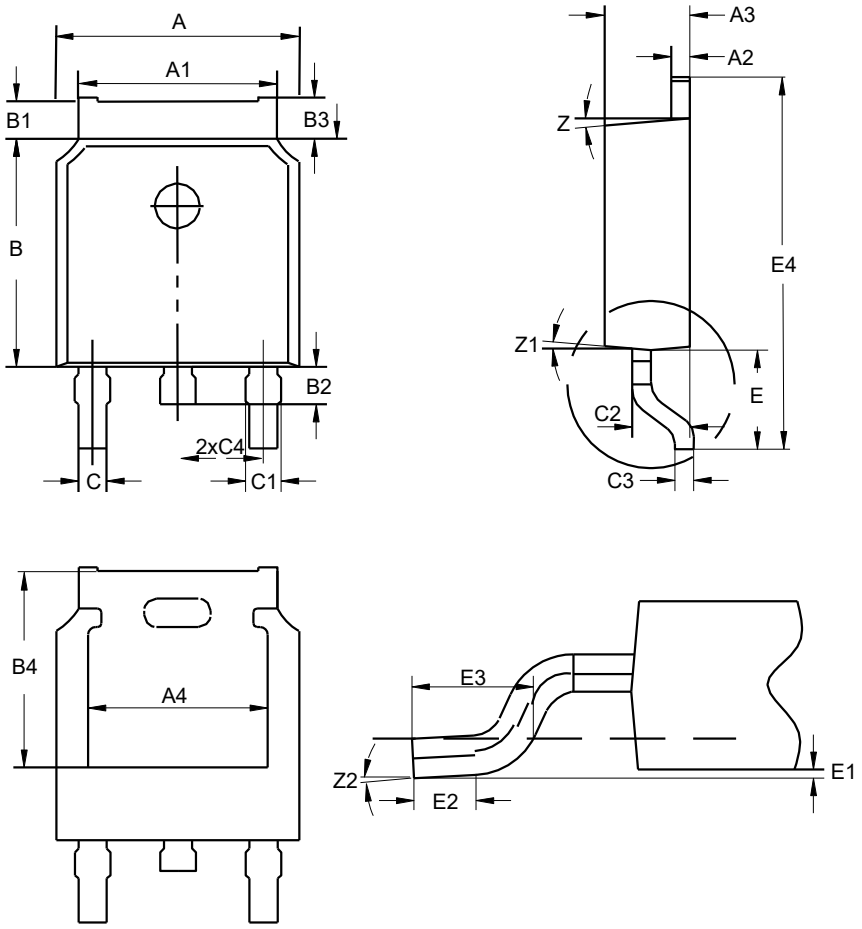


Fig. 13 - Drain Current Derating



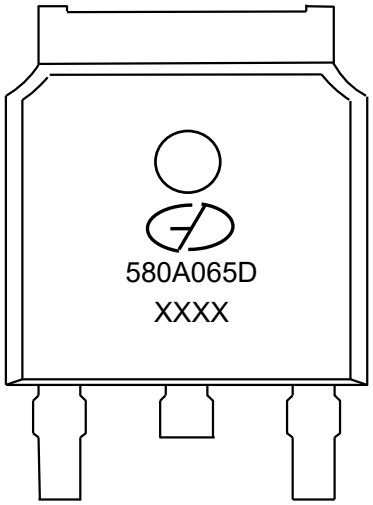
Package Outline Dimensions (Unit: millimeters)

TO-252(D-PAK)



TO-252							
	Min.	Nom.	Max.		Min.	Nom.	Max.
A	6.34	6.54	6.74	C2	1.34	1.54	1. 74
A1	5.2	5.3	5.4	C3	0.4	0.5	0.6
A2	0.4	0.5	0.6	C4	2.09	2.29	2.49
A3	2.08	2.28	2.48	E	2.6	2.9	3.2
A4	4.6	4.8	5.0	E1	0	-	0.15
B	5.8	6.1	6.4	E2	0.7	-	-
B1	0.82	1.02	1.22	E3	1.3	1.6	1.9
B2	0.8	1	1.2	E4	9.8	10.1	10.4
B3	0.9	1.1	1.3	Z	-	7°	-
B4	5.05	5.25	5.45	Z1	-	7°	-
C	0.66	0.76	0.86	Z2	0°	-	10°
C1	0.65	0.85	1.05	-	-	-	-

Marking Outline



Part Name: GMN580A065D

1. Logo Mark:
2. P/N Mark: 580A065D
3. Date Code: XXXX

Revision History

Version	Date	Major Changes
Rev.A	2025.04.07	Official Release
Rev.B	2025.08.05	Update VGS from ±20V to ±30V

Disclaimers

These materials are intended as a reference to assist our customers in the selection of the Suzhou Good-Ark product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Suzhou Good-Ark Electronics Co., Ltd.or a third party.

Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.

All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Suzhou Good-Ark Electronics Co., Ltd. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Suzhou Good-Ark Electronics Co., Ltd. or an authorized Suzhou Good-Ark Electronics Co., Ltd. for the latest product information before purchasing a product listed herein. The information described here may contain technical inaccuracies or typographical errors. Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors. Please also pay attention to information published by Suzhou Good-Ark Electronics Co., Ltd. by various means, including our website home page.
(http://www.goodark.com)

When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.

The prior written approval of Suzhou Good-Ark Electronics Co., Ltd. is necessary to reprint or reproduce in whole or in part these materials.

Please contact Suzhou Good-Ark Electronics Co., Ltd. or an authorized distributor for further details on these materials or the products contained herein.